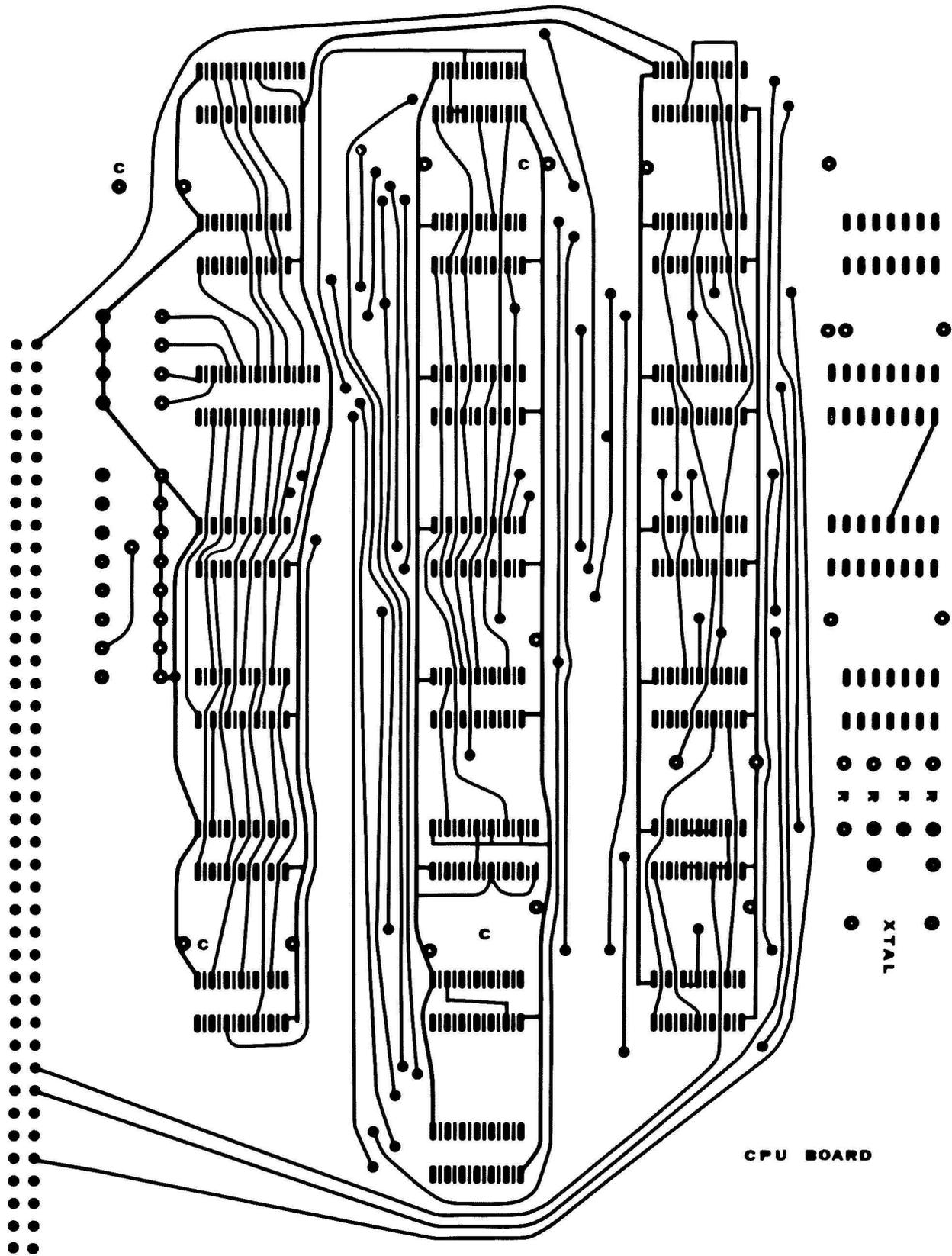
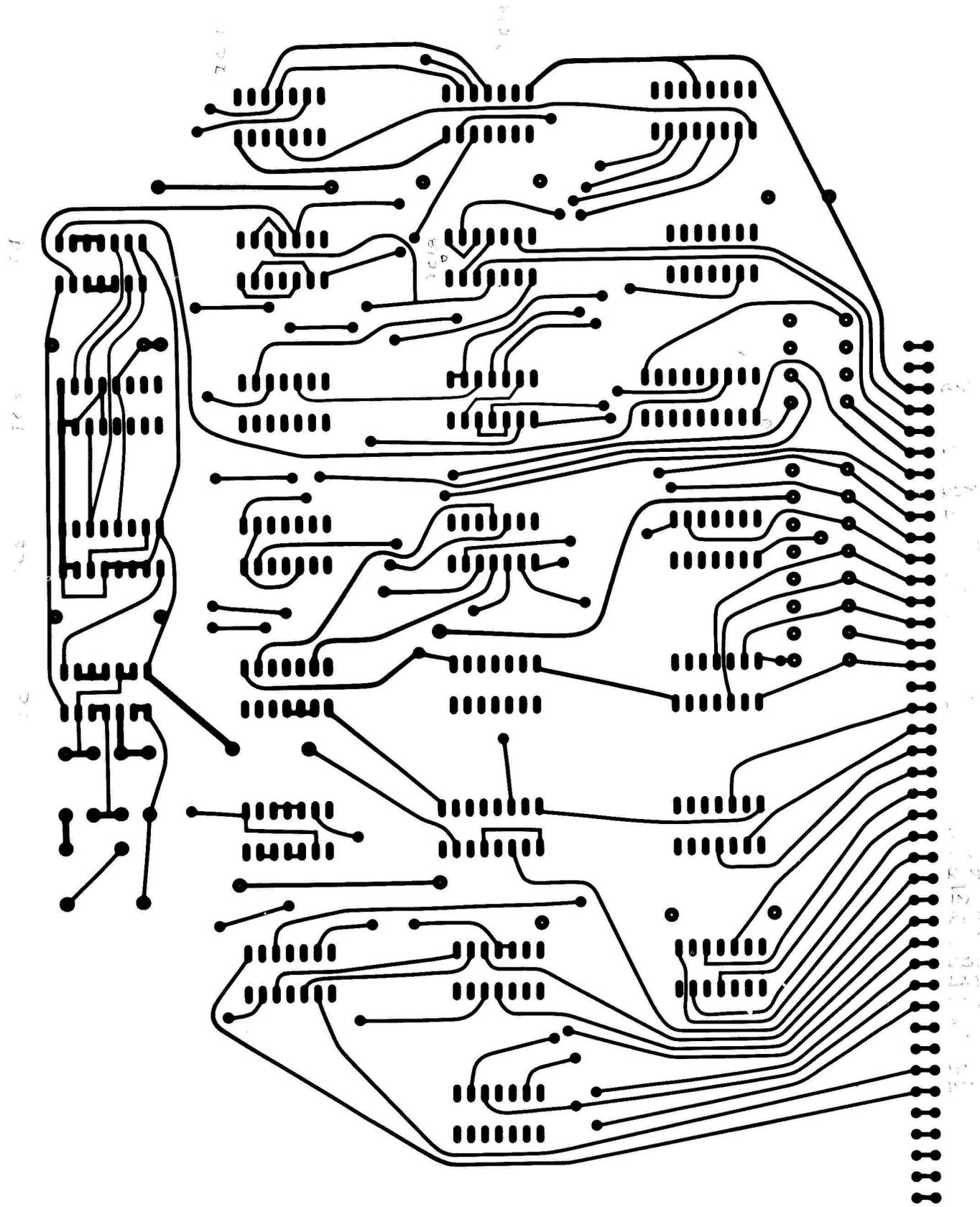


# **PRINTED CIRCUIT BOARD PATTERNS**

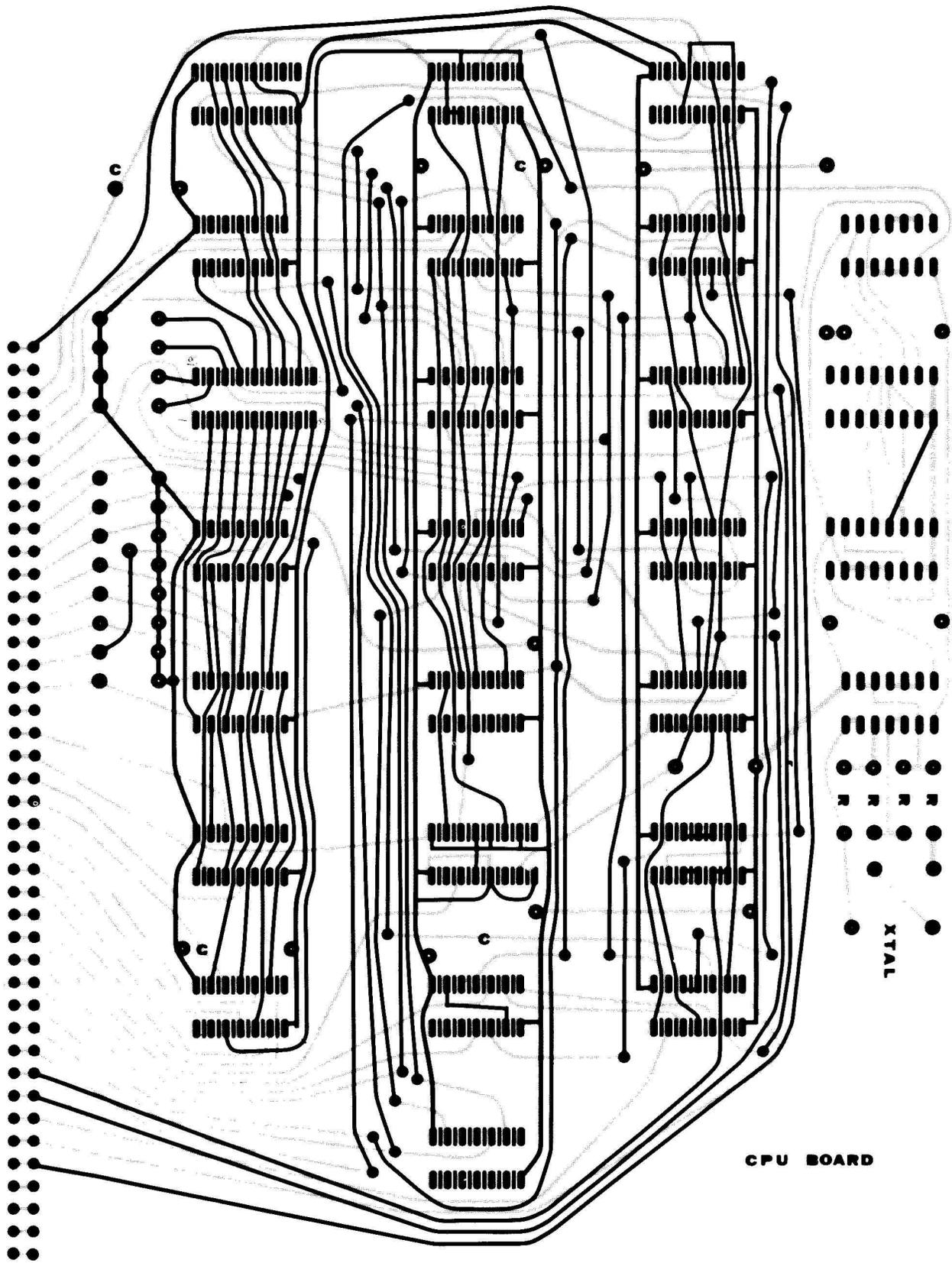


CPU BOARD - SIDE A

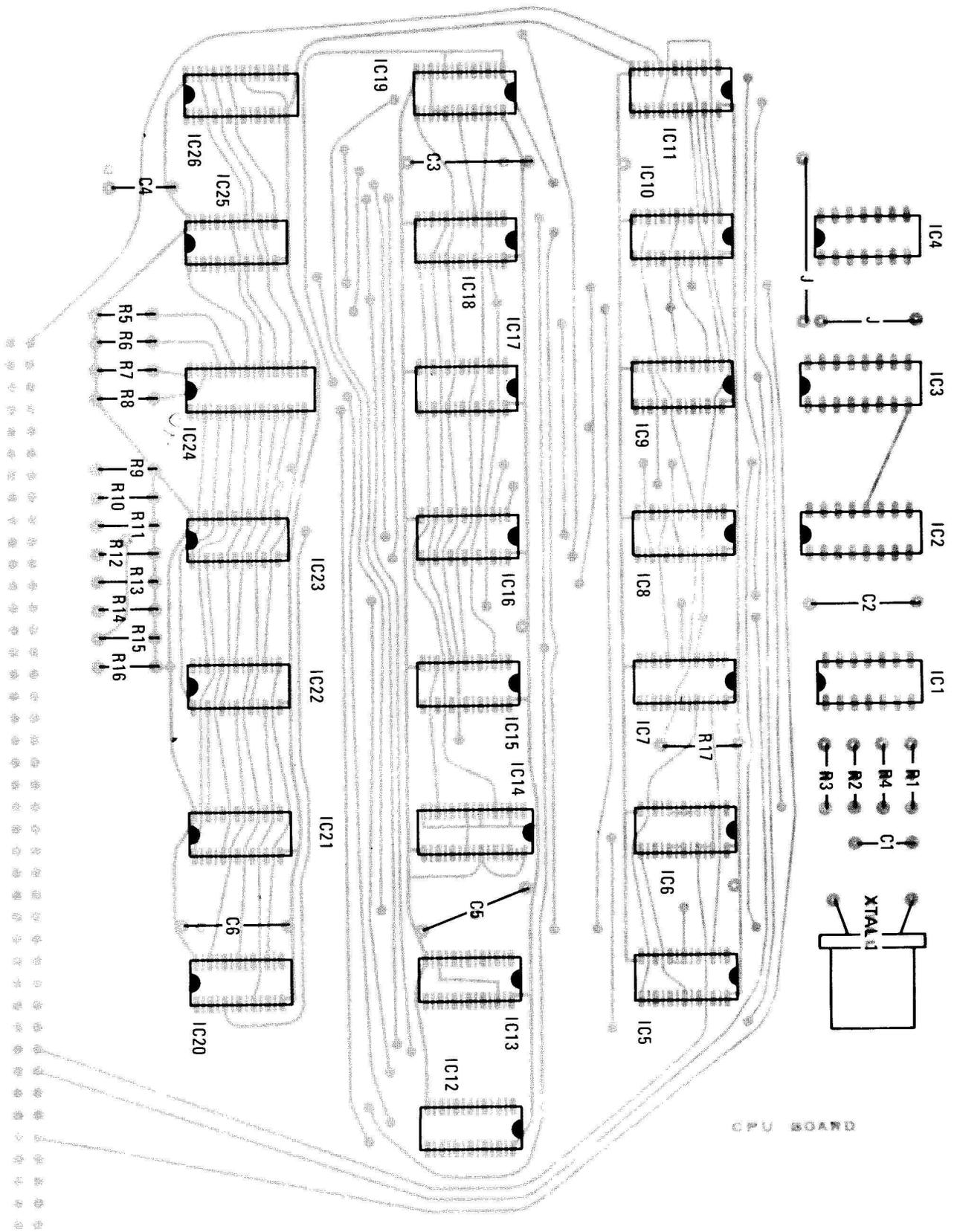
CPU BOARD



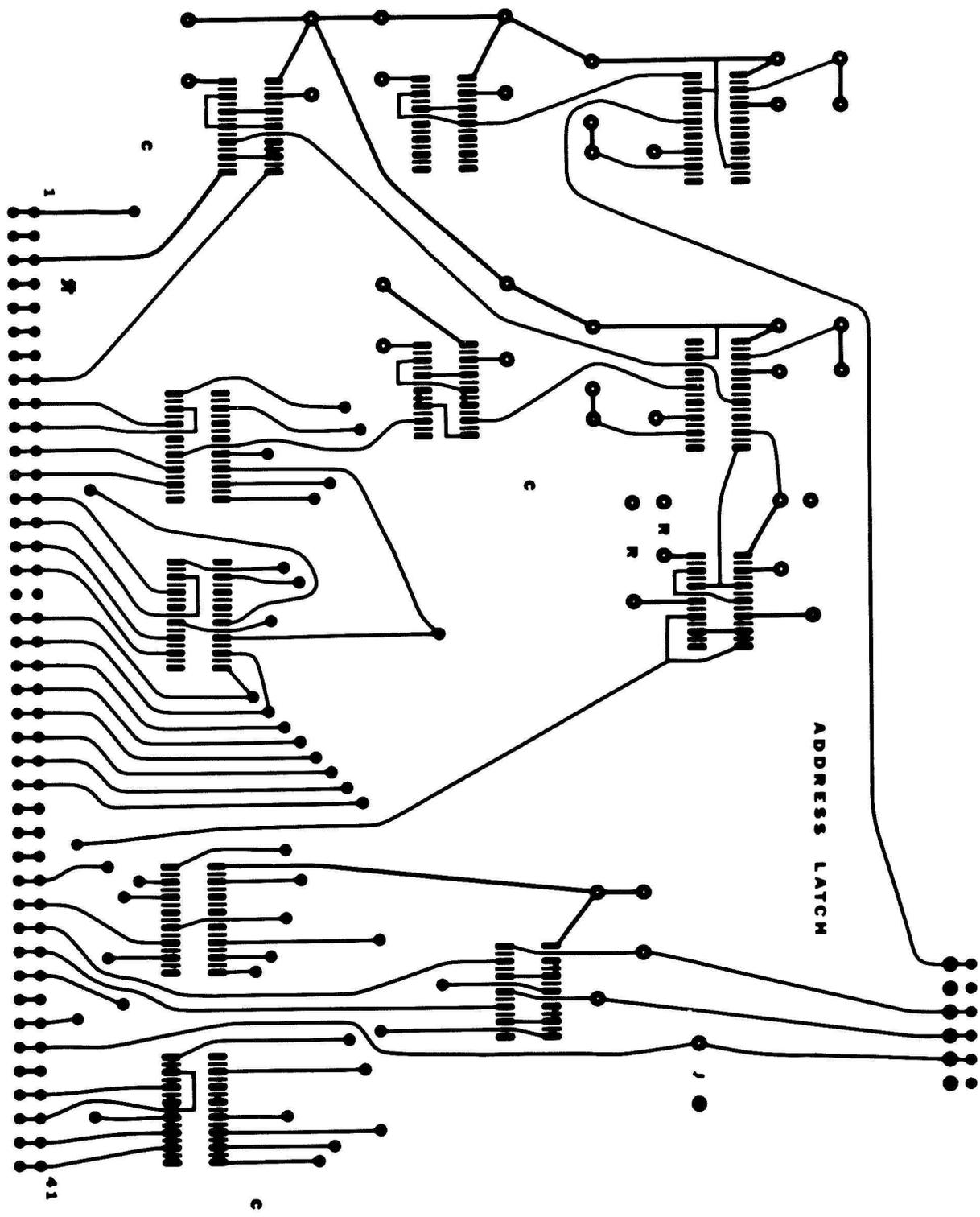
CPU BOARD - SIDE B



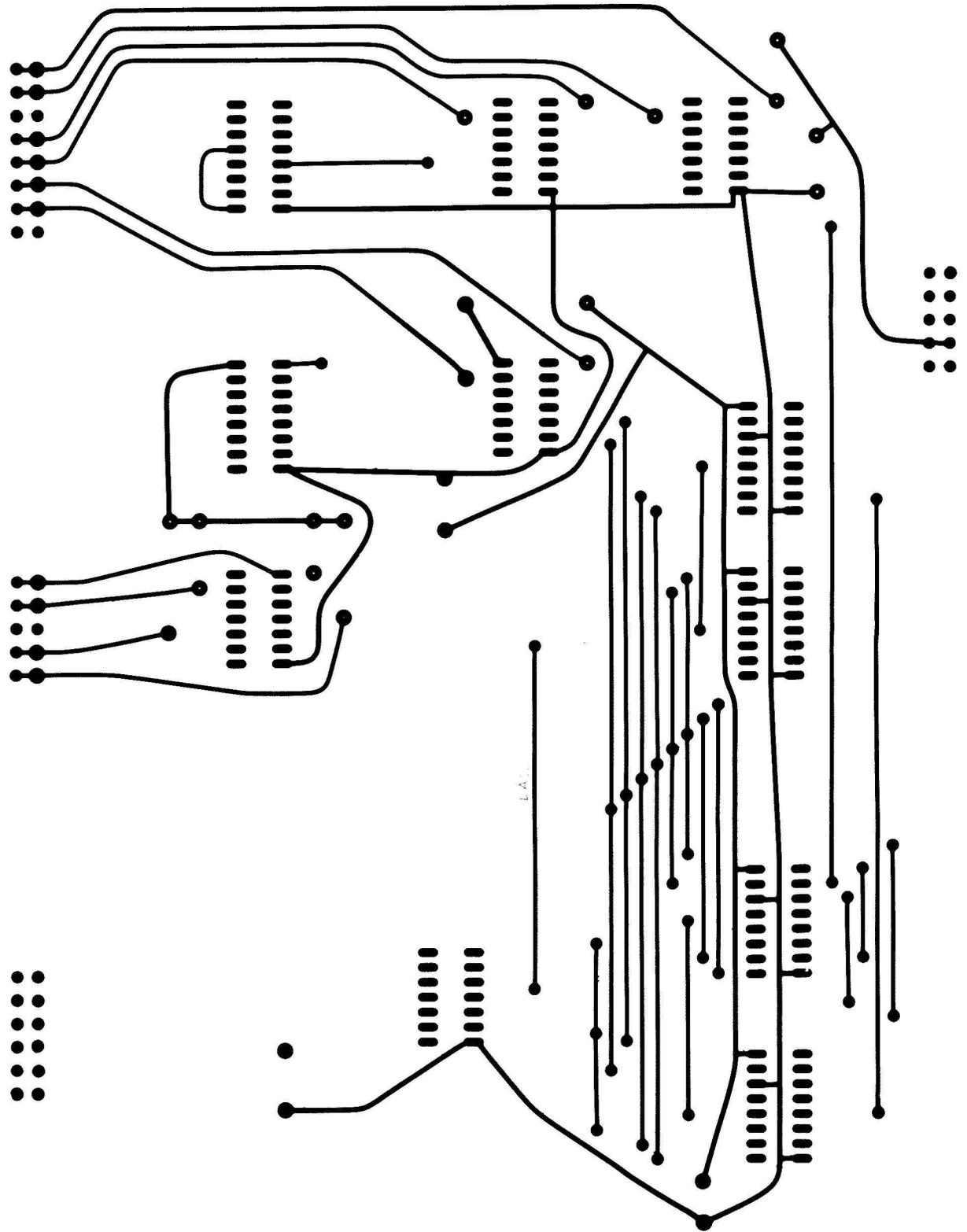
CPU BOARD – HOW FOIL PATTERNS OVERLAP



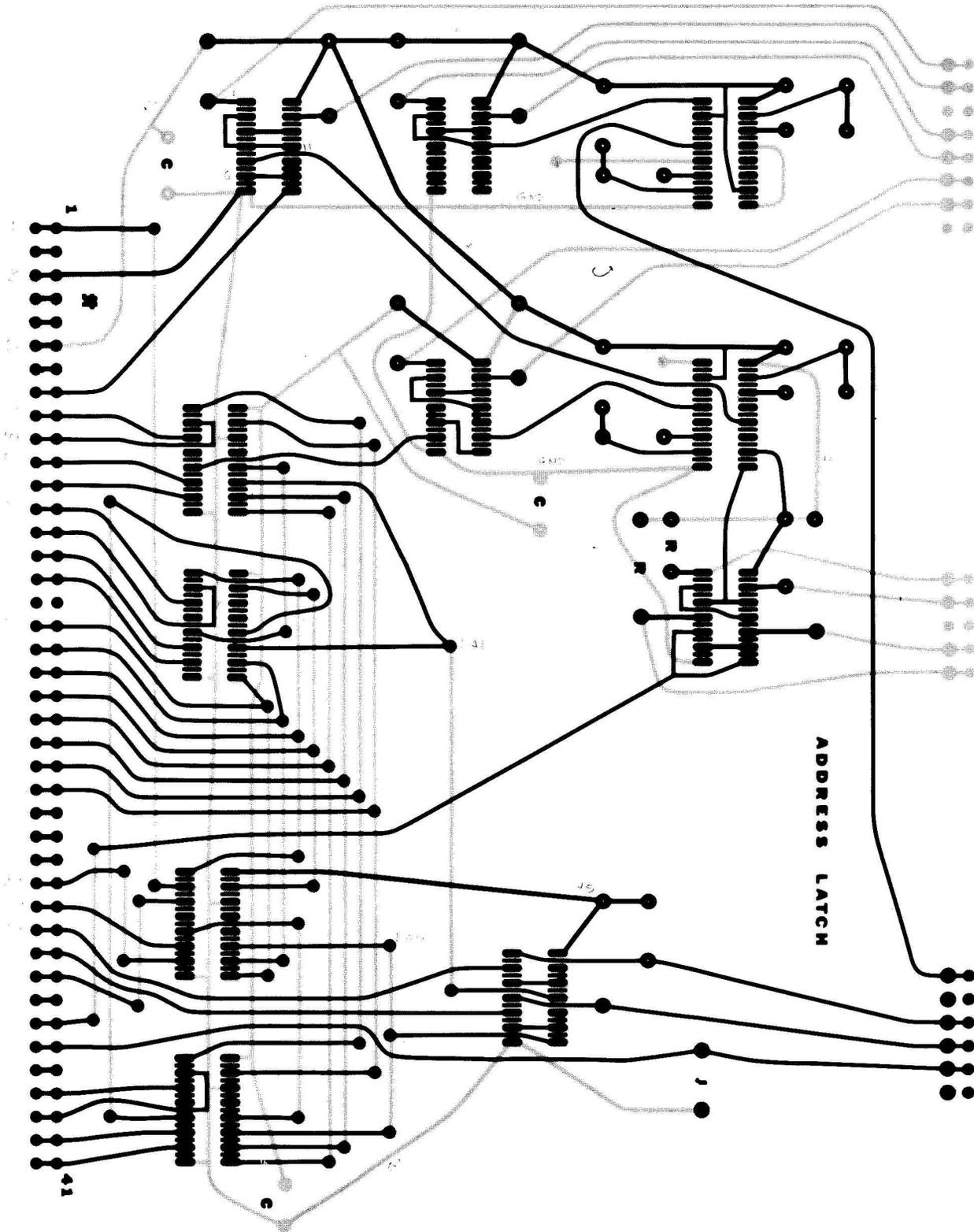
CPU BOARD – PARTS LAYOUT



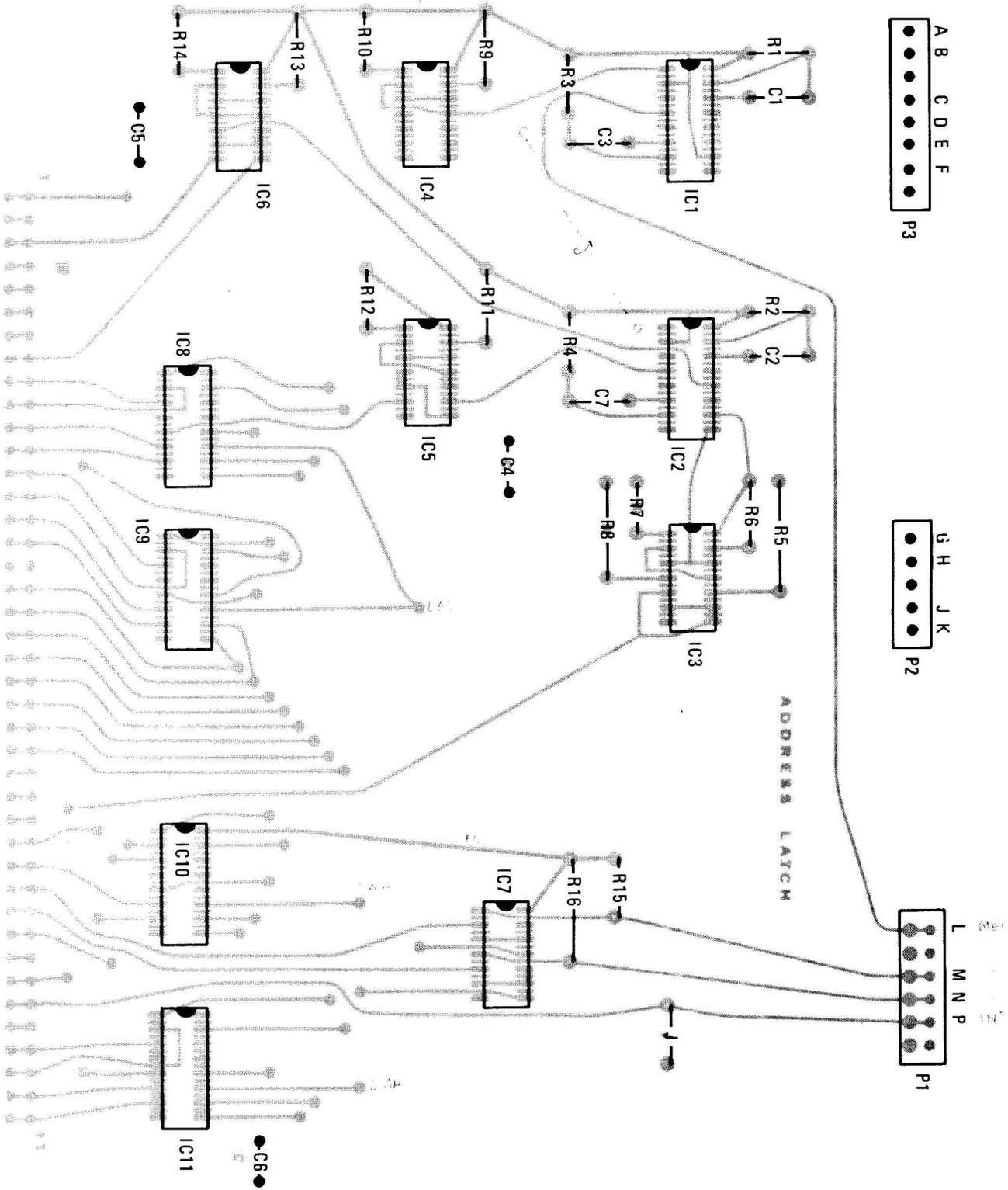
ADDRESS LATCH - SIDE A



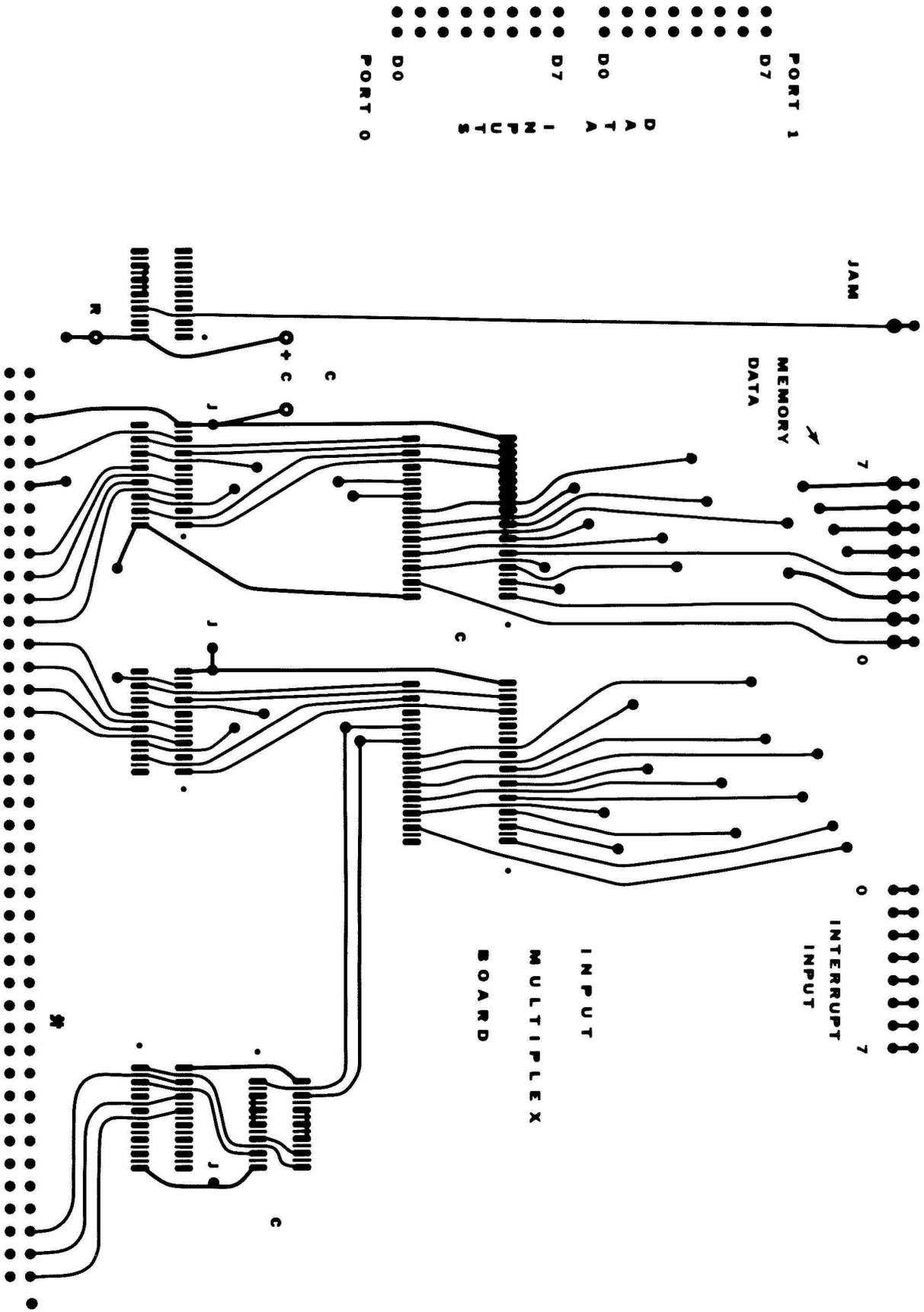
ADDRESS LATCH - SIDE B



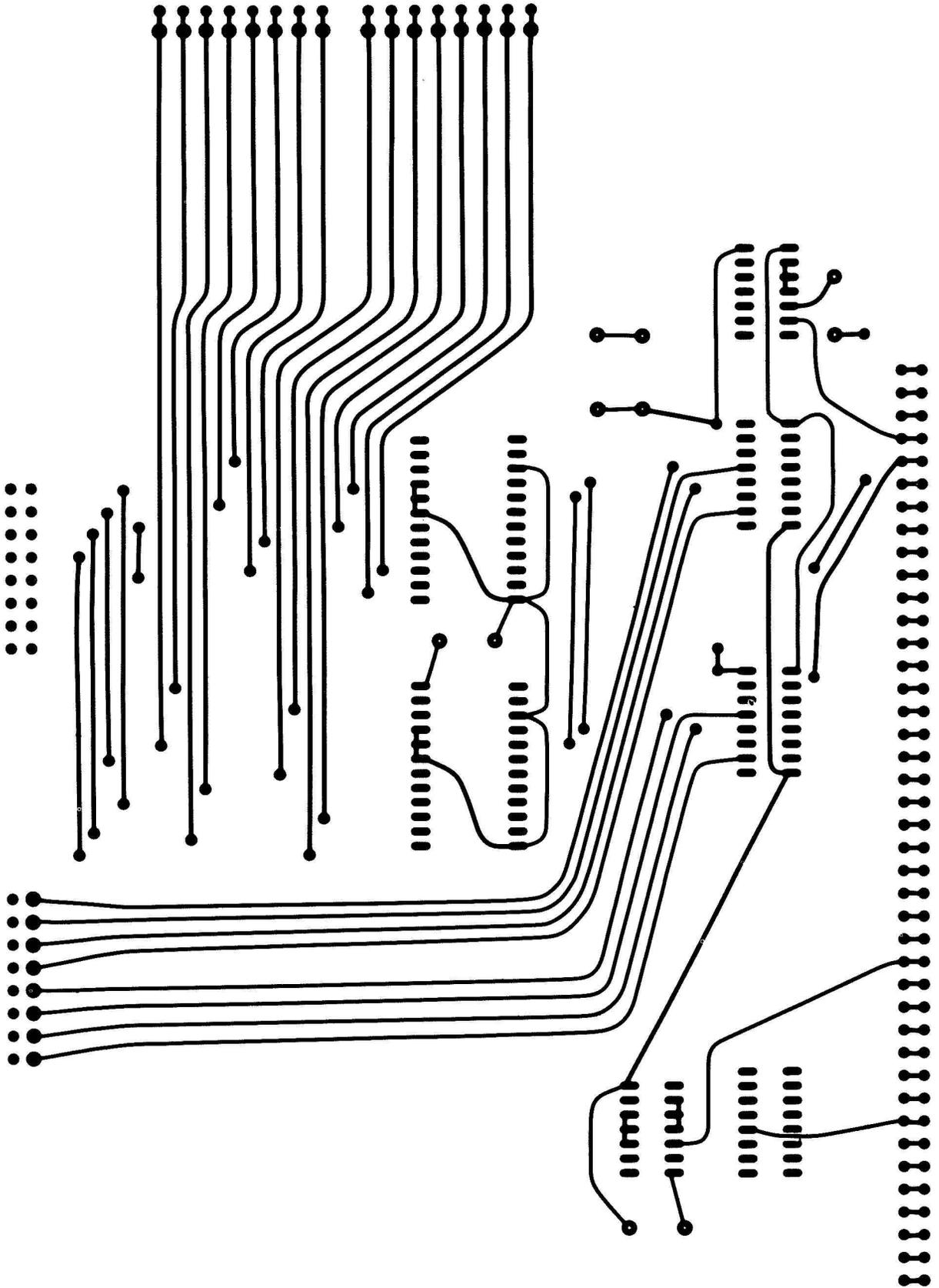
ADDRESS LATCH – HOW FOIL PATTERNS OVERLAP



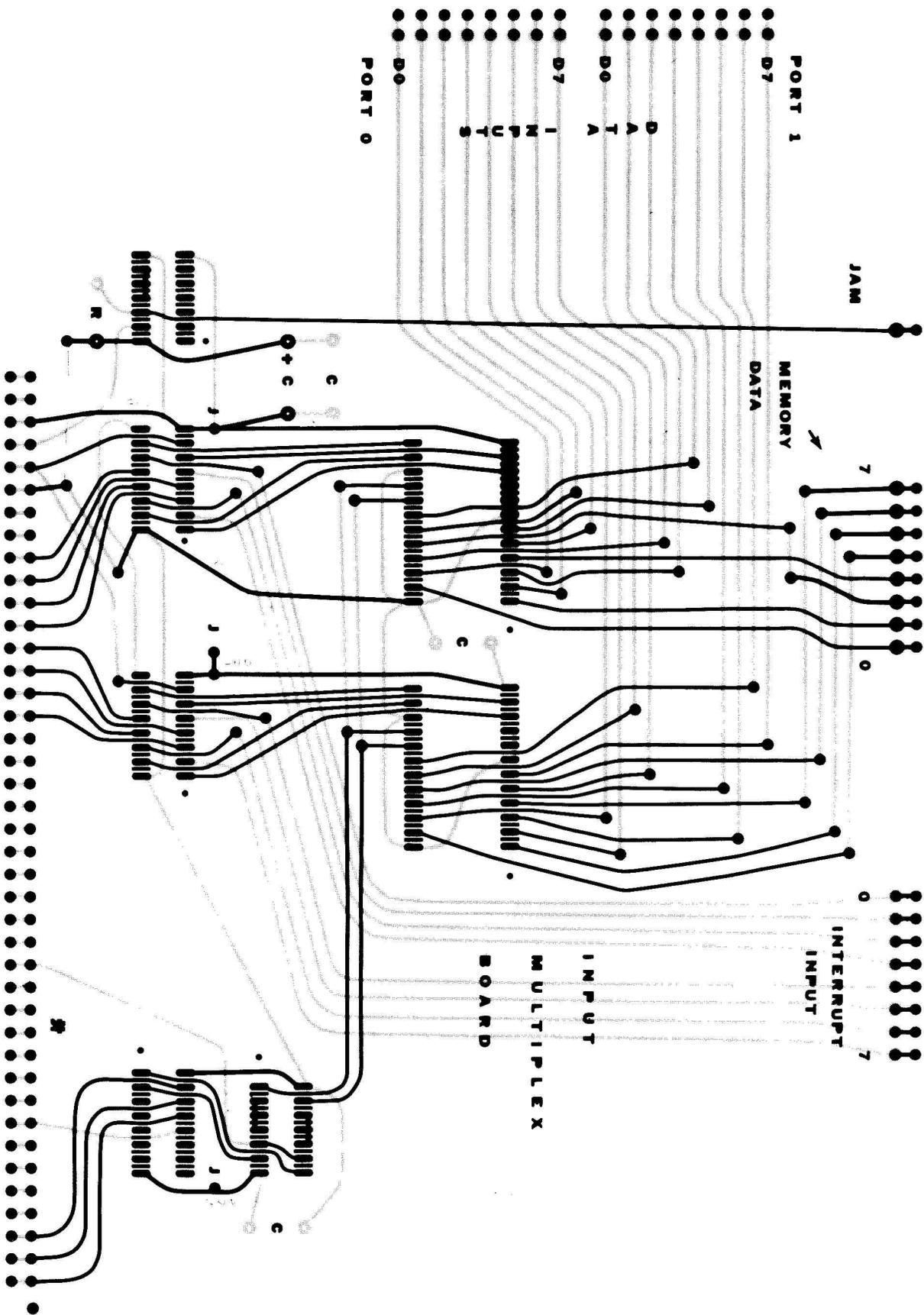
ADDRESS LATCH – PARTS LAYOUT



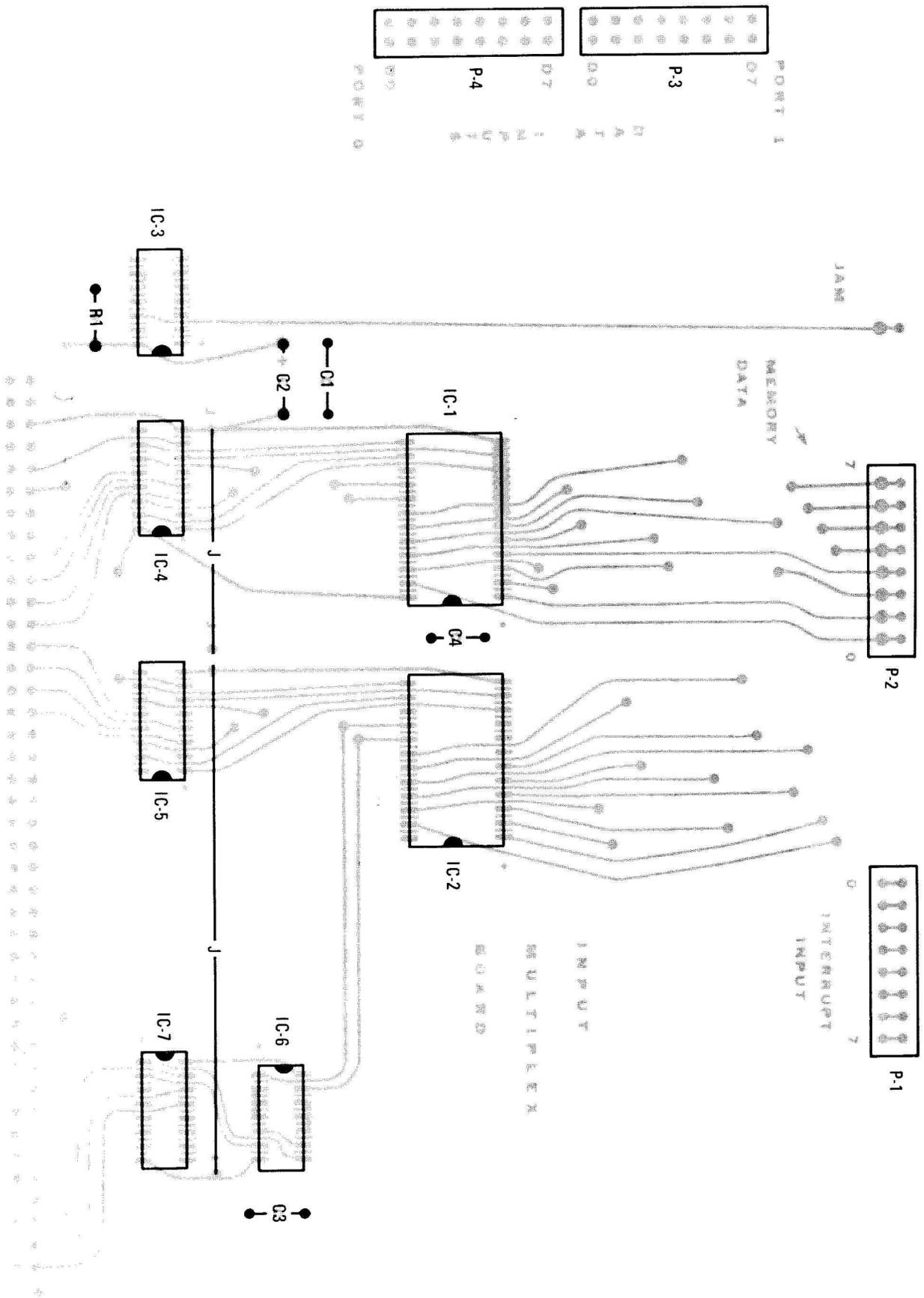
DATA INPUT MPX BOARD - SIDE A



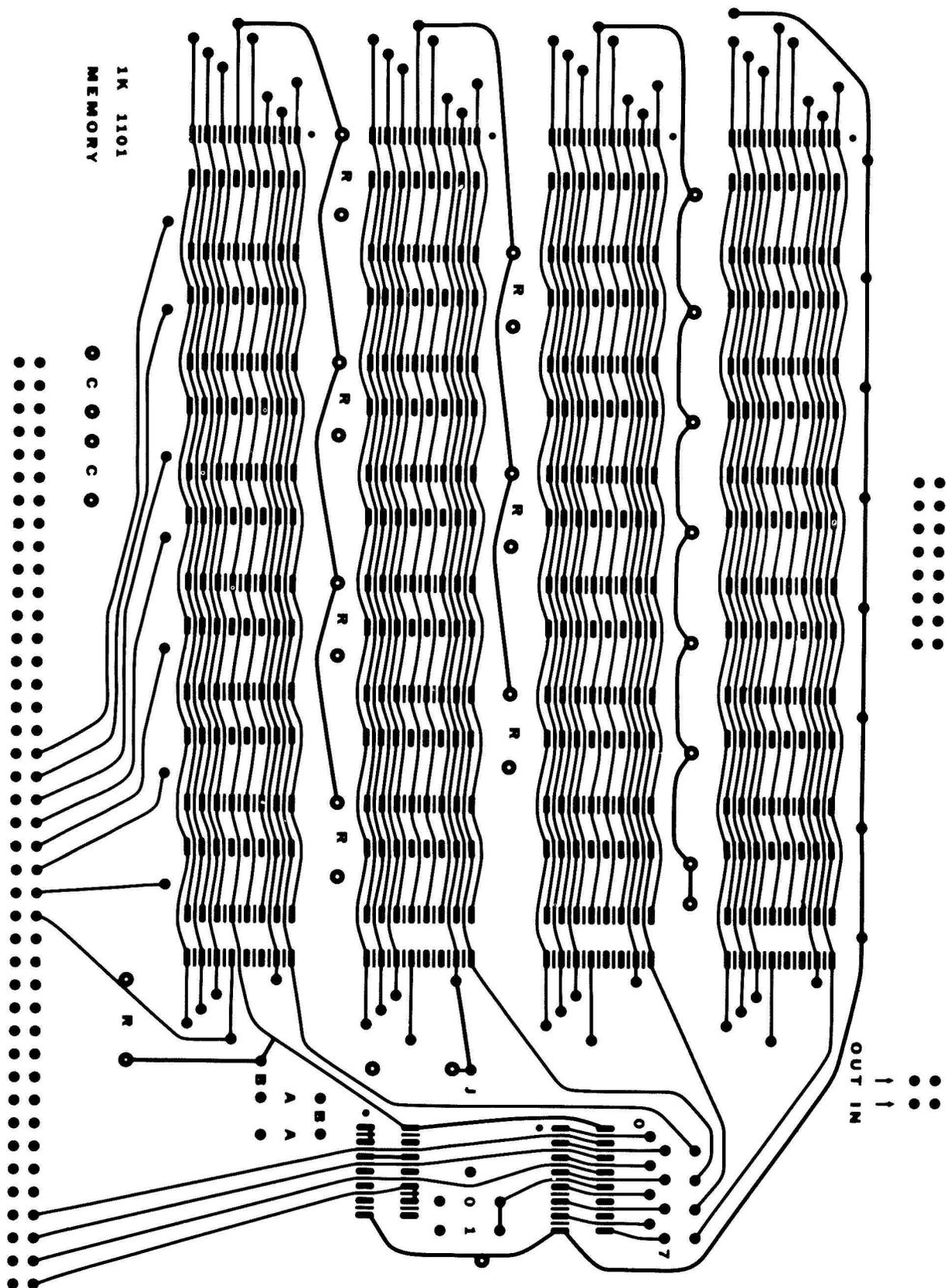
DATA INPUT MPX BOARD – SIDE B



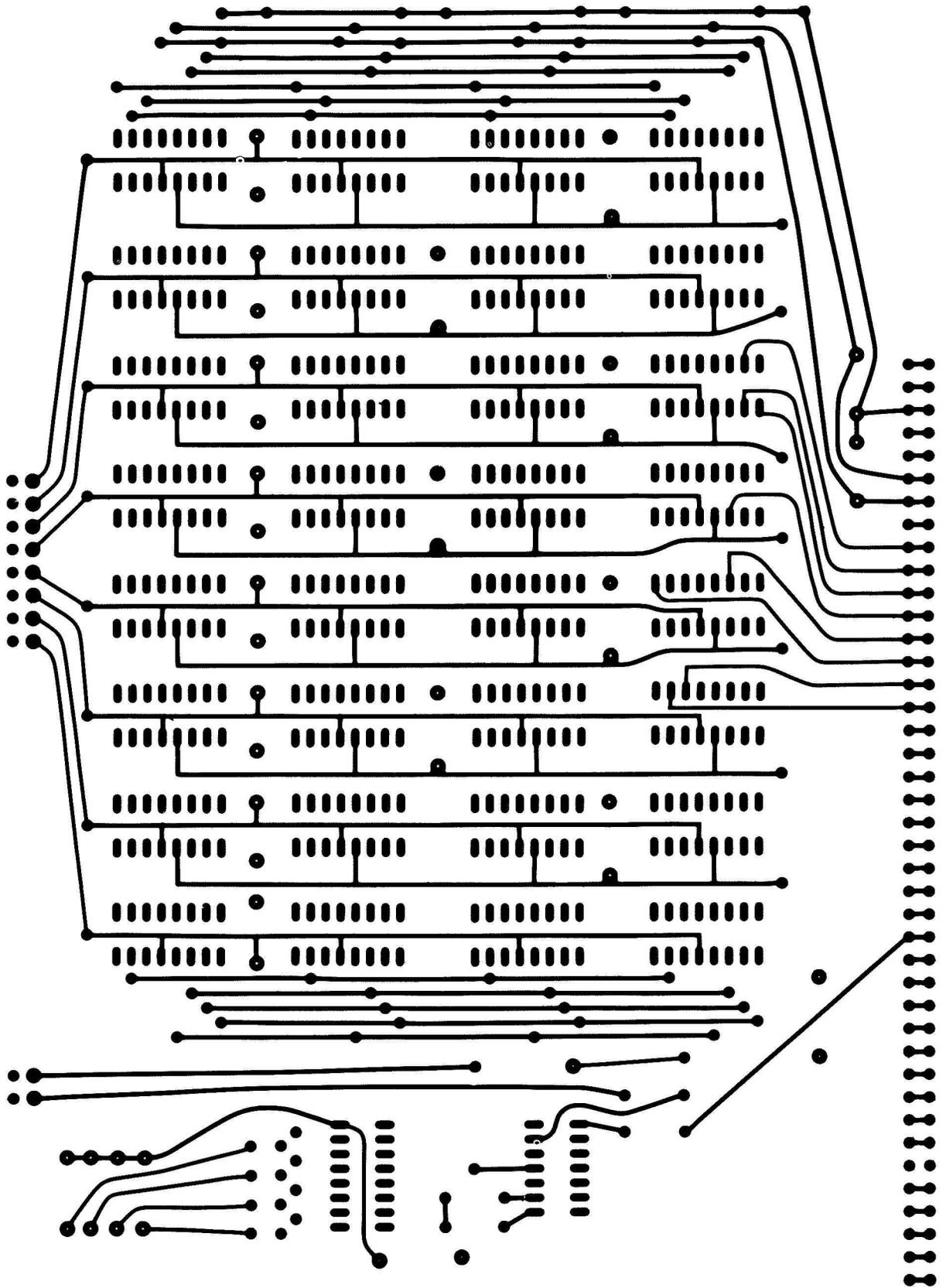
DATA INPUT MPX BOARD – HOW FOIL PATTERNS OVERLAP



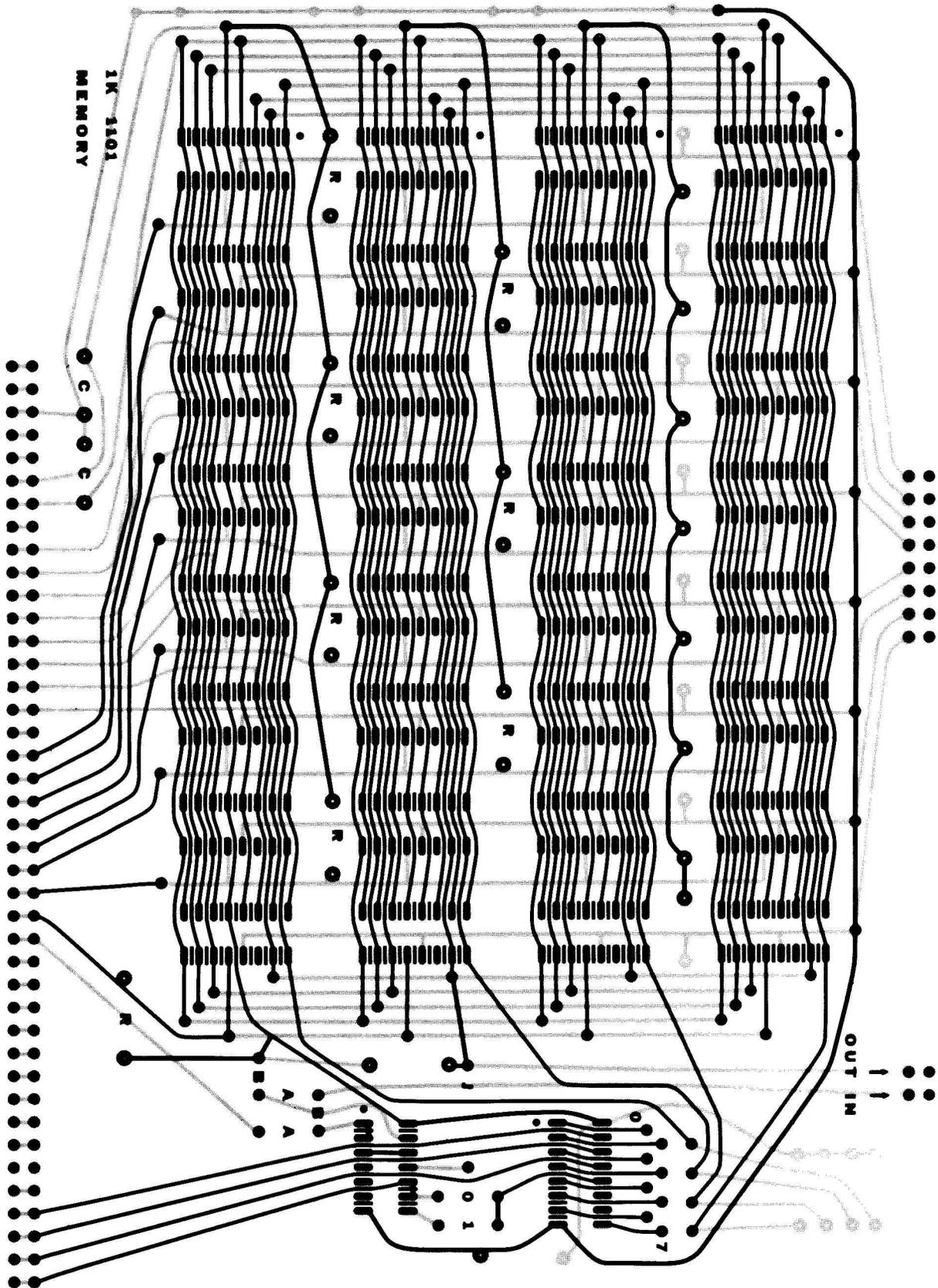
DATA INPUT MPX BOARD – PARTS LAYOUT



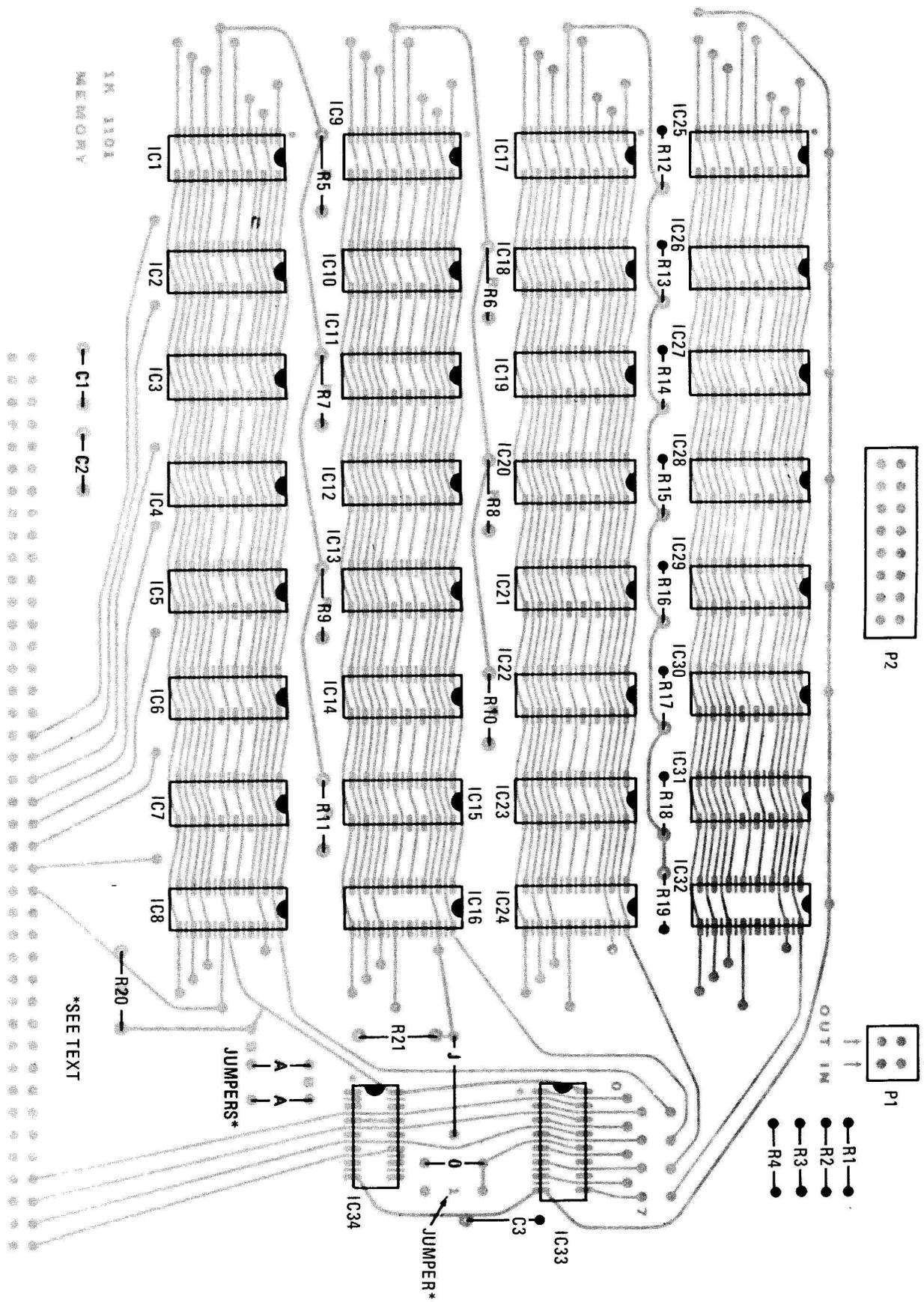
MEMORY - SIDE A



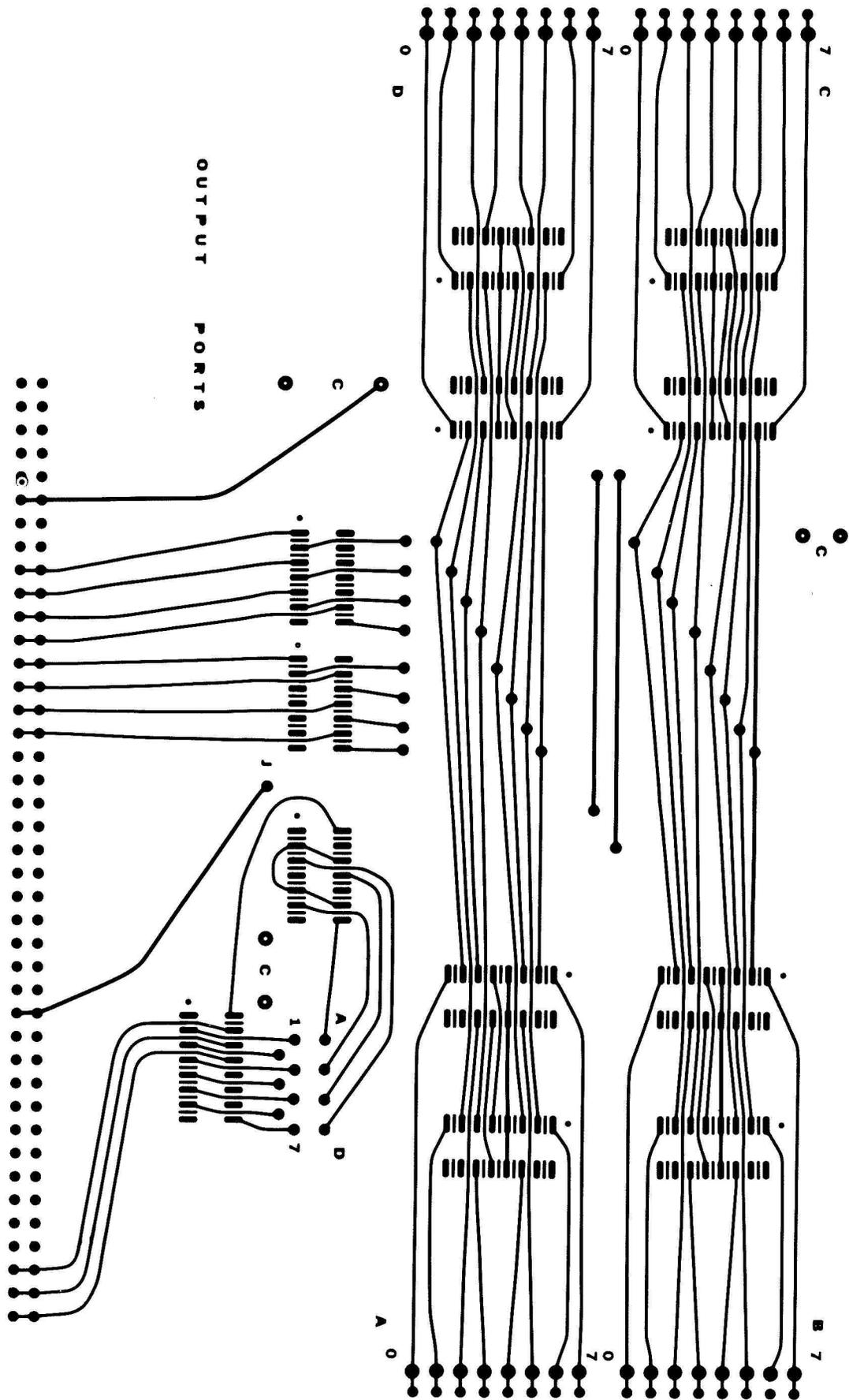
MEMORY - SIDE B



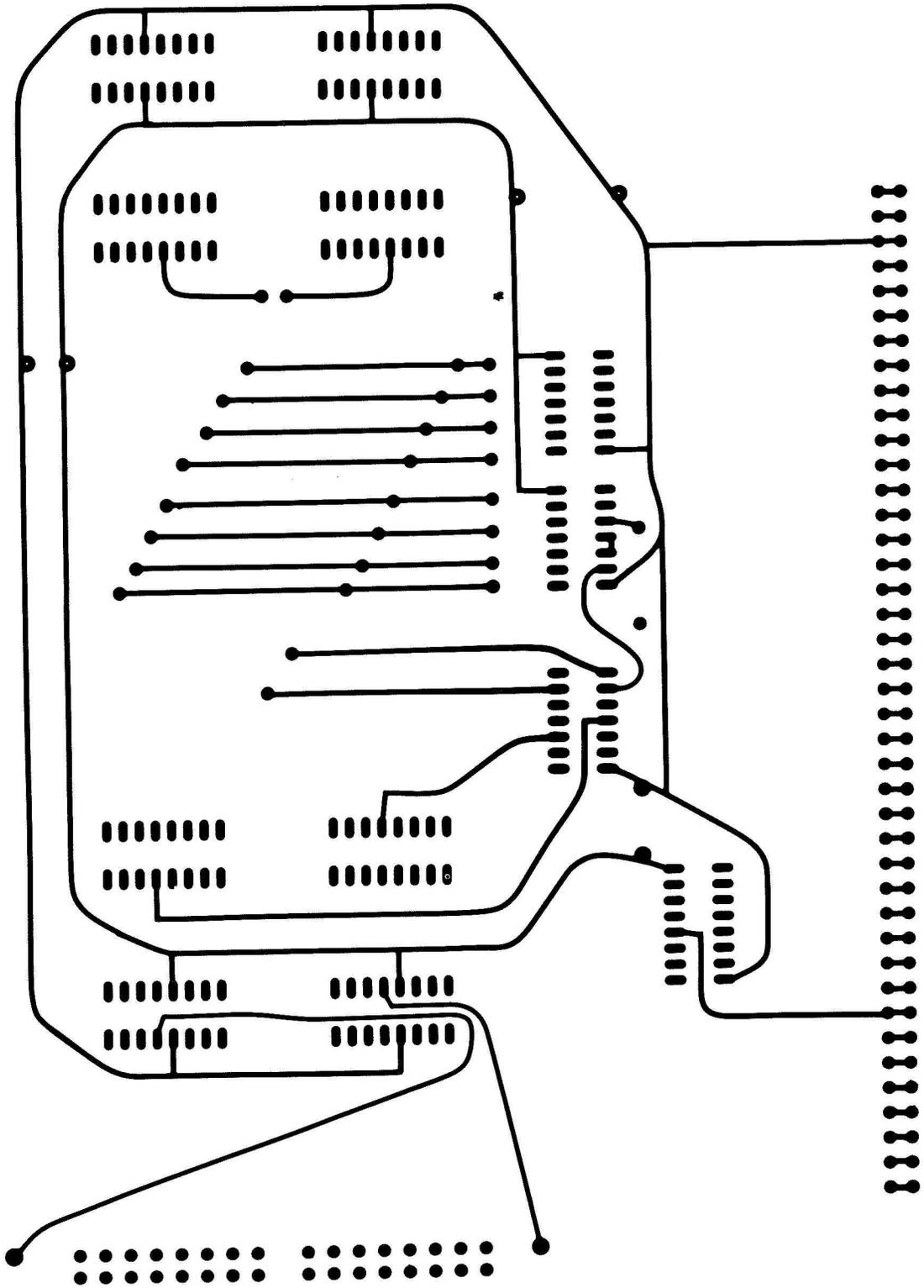
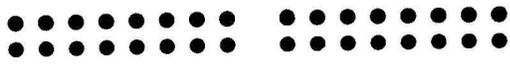
MEMORY - HOW FOIL PATTERNS OVERLAP



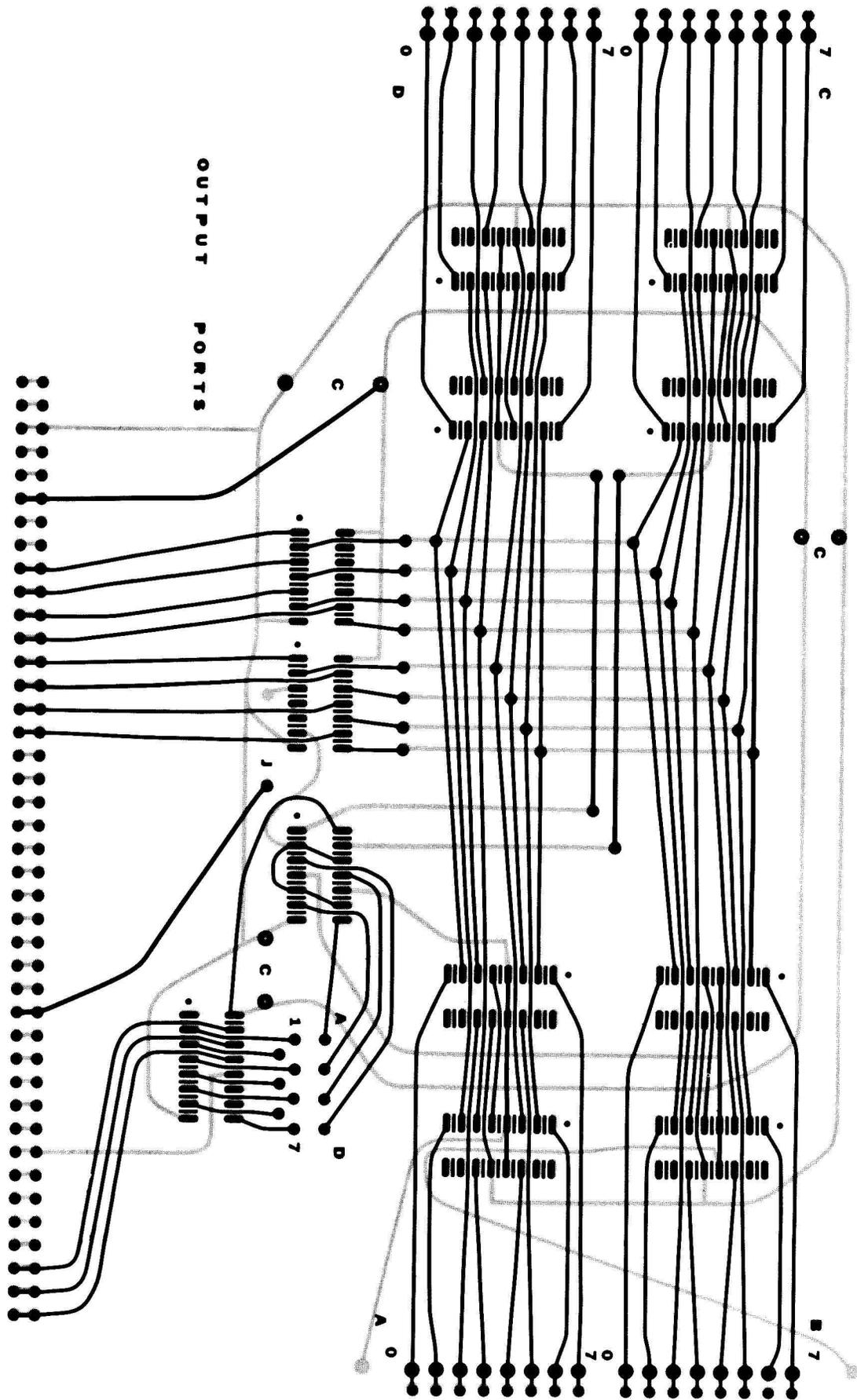
MEMORY - PARTS LAYOUT



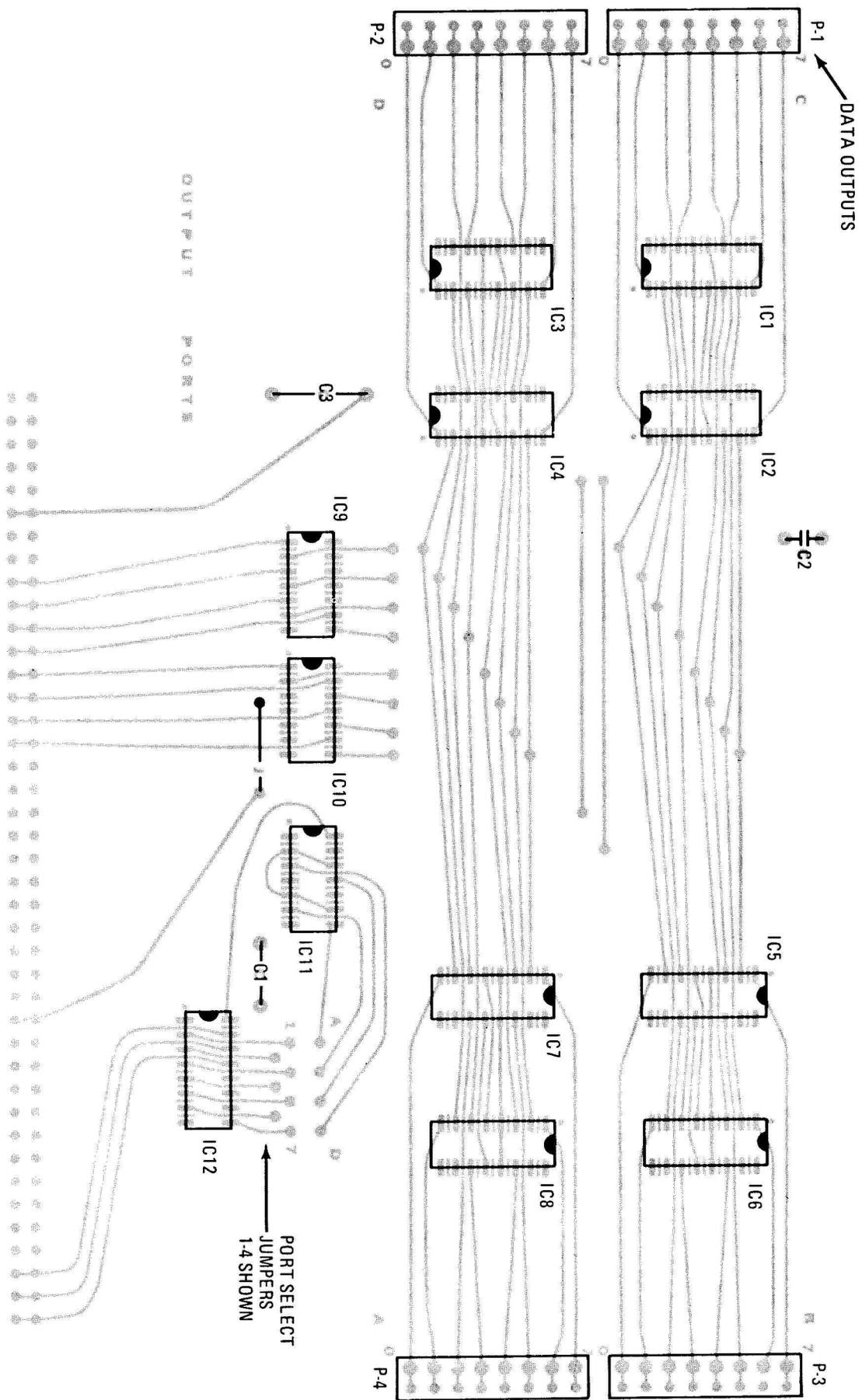
OUTPUT PORTS - SIDE A



OUTPUT PORTS – SIDE B

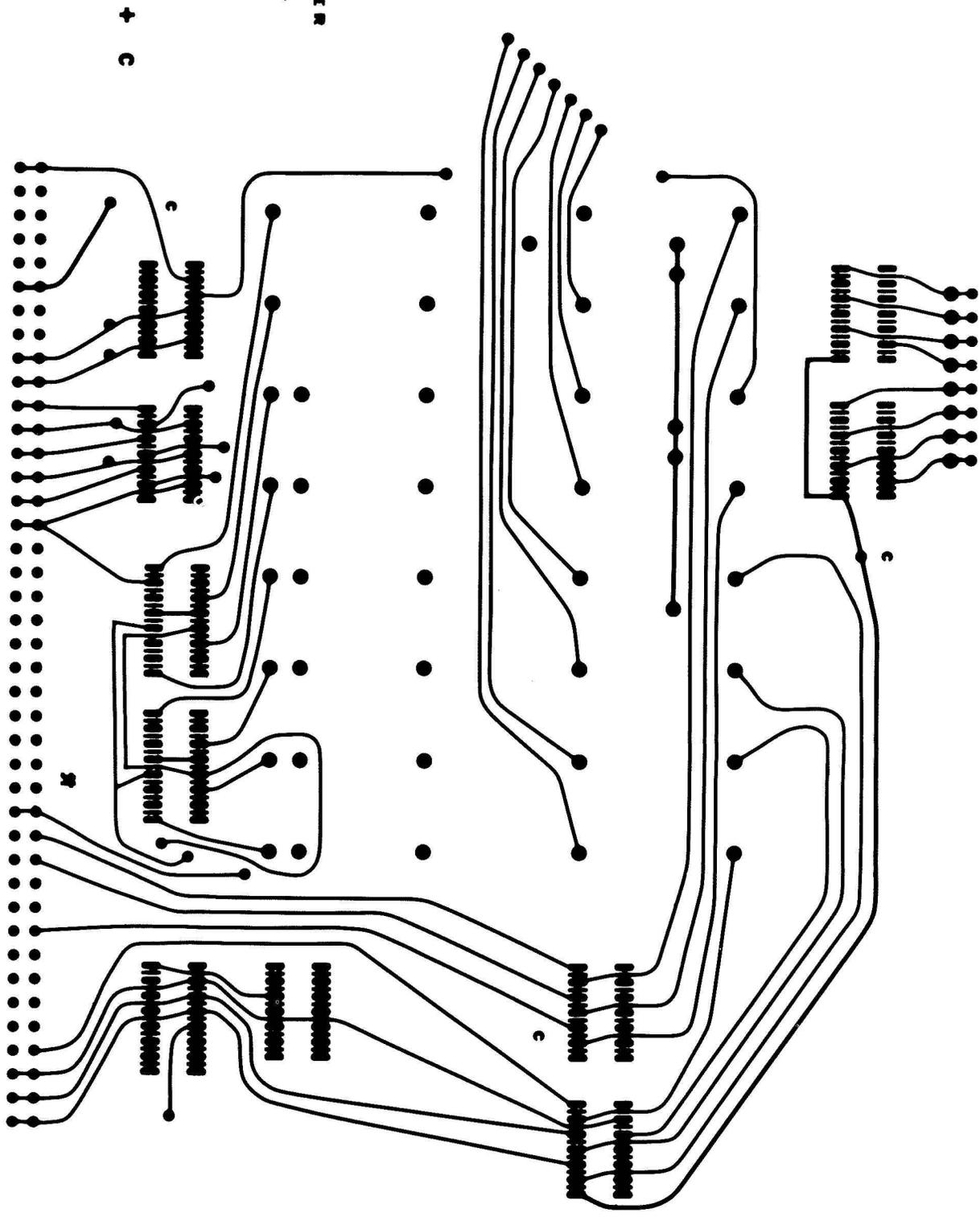


OUTPUT PORTS — HOW FOIL PATTERNS OVERLAP

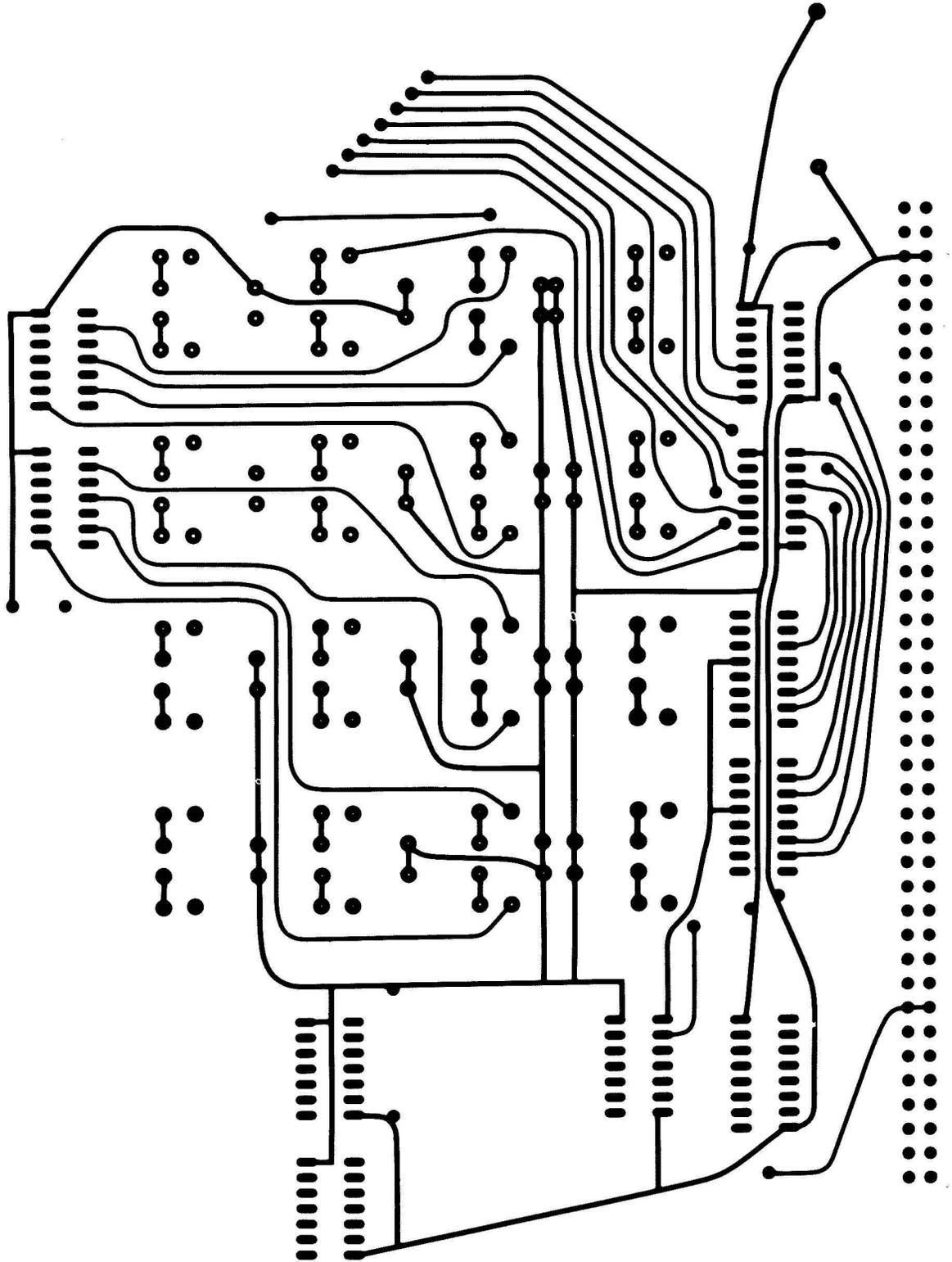


OUTPUT PORTS – PARTS LAYOUT

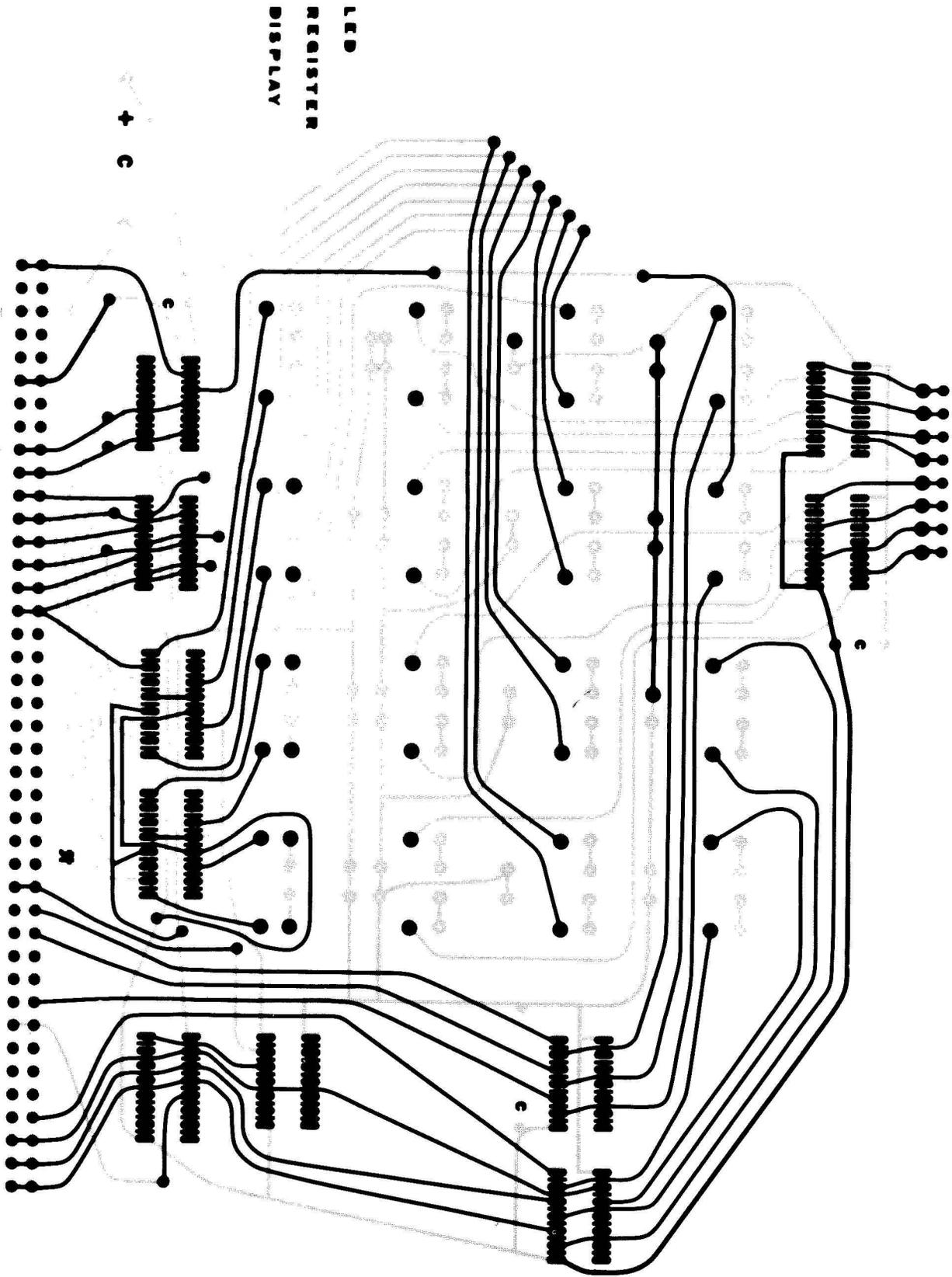
LED  
REGISTER  
DISPLAY



LED REGISTER DISPLAY - SIDE A



LED REGISTER DISPLAY – SIDE B



LED REGISTER DISPLAY – HOW FOIL PATTERNS OVERLAP

